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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	Not Yet Assigned 10/814,640
				Filing Date	April 1, 2004
				First Named Inventor	Sukesh Sandhu
				Art Unit	N/A 2825
				Examiner Name	Not Yet Assigned C. Everhart
Sheet	1	of	1	Attorney Docket Number	M4065.0224/P224-A

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
CME	AA*	5,471,364	11/28/1995	Summerfelt, et al.	—
CME	AB*	5,888,870	03/30/1999	Gardner, et al.	—
CME	AC*	5,348,894	09/20/1994	Gnade, et al.	—
CME	AD*	5,674,788	10/07/1997	Wristers, et al.	—
CME	AE*	5,124,174	06/23/1992	Musket, et al.	—

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
CME	CA*	S. Wolf and R.N. Tauber, "Ion Implantation for VLSI," <i>Silicon Processing for the VLSI Era</i> , Volume 1 – Process Technology, Chapter 9, pages 280-327 (1986)	
CME	CB*	S. Wolf and R.N. Tauber, "Suprem III Models: Ion Implantation," <i>Silicon Processing for the VLSI Era</i> , Volume II, Chapter 9.2.2, pages 658-661 (1986).	

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C. Everhart

9-13-04